

Abstract

~~Circuit for generating an asynchronous signal pulse~~

The invention relates to a circuit for generating an asynchronous signal pulse in an integrated circuit.

In order to generate a pulsed signal with a desired active state without a great outlay on circuitry, the circuit according to the invention is characterized by a first and a second transistor (2, 3) in the integrated circuit, which are connected in series between a supply potential (U_{DD}) and ground (GND), firstly a control pulse (A) having the predetermined duration being present at a control connection (G1) of the first transistor (2) and then a control pulse (B) being present at a control connection (G2) of the second transistor (3), with the result that, for the predetermined duration, firstly the first transistor (2) and then the second transistor (3) is turned on, and a resistor (6, 7) for the definition of the active signal state, which is connected outside the integrated circuit in parallel with one of the two transistors (2, 3) in the integrated circuit either between the supply potential (U_{DD}) and the connecting point (4) or between ground (GND) and the connecting point (4).

~~Figure 1a~~

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List of reference symbols

- 1 Chip of integrated circuit
- 2 First transistor of the push-pull stage
- 3 Second transistor of the push-pull stage
- 4 Connection pad
- 5 Board for external circuit
- 6 Pull-up resistor
- 7 Pull-down resistor

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